

Examiner's amendment

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Jack Freedman on 1/27/05.

The application's claim 25 C been amended to read as; "the second gate structure having a second fermi level that differs from said first fermi level.", after the text "second vertical edge, "

The title has been amended to read as; "DOUBLE GATED VERTICAL TRANSISTOR WITH DIFFERENT FIRST AND SECOND GATE MATERIALS"

Statement of reasons for allowance

Claims 25-40 have been allowed.

Although double-gated transistors with different lateral gate materials or gates with different fermi levels is known in the art, the claimed transistor with two vertical gates of different materials or fermi levels, however, was neither rendered obvious or taught by the prior arts.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Fetsum Abraham whose telephone number is: 571-272-1911. The examiner can normally be reached on 8:00 - 18:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

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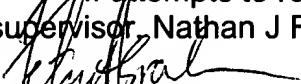
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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan J Flynn can be reached on 571-272-1915.


Fetsum Abraham
1/27/05